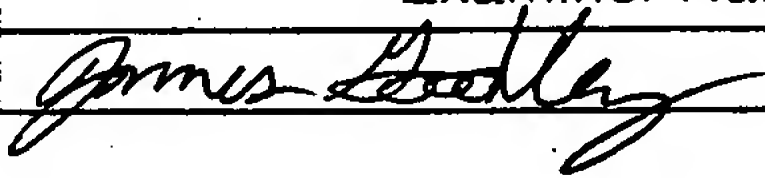
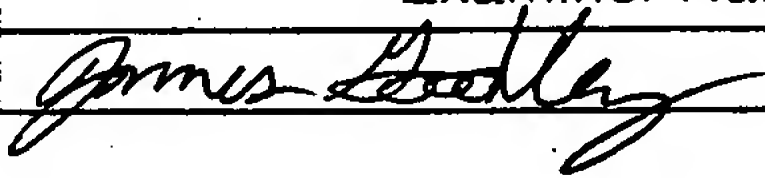
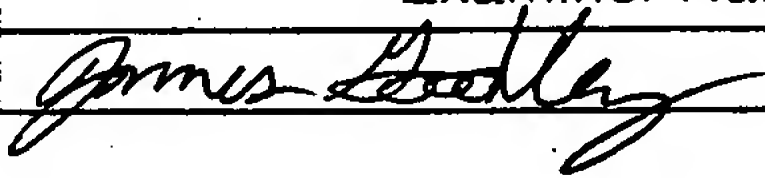


## ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

<b>Title of Invention</b>	[LOW POWER CONSUMPTION CIRCUIT AND DELAY CIRCUIT THEREOF]																
 Application Number : Confirmation Number: First Named Applicant: Hong-Gee Fang Attorney Docket Number: 11425-US-PA Art Unit: Examiner: Search string: ( 6188293 ).pn																	
 <b>US Patent Documents</b>  Note: Applicant is not required to submit a paper copy of cited US Patent Documents																	
<table border="1"><thead><tr><th>init</th><th>Cite.No.</th><th>Patent No.</th><th>Date</th><th>Patentee</th><th>Kind</th><th>Class</th><th>Subclass</th></tr></thead><tbody><tr><td>JG</td><td>1</td><td>6188293</td><td>2001-02-13</td><td>Miyagi , et al.</td><td></td><td>331</td><td>57</td></tr></tbody></table>		init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass	JG	1	6188293	2001-02-13	Miyagi , et al.		331	57
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